

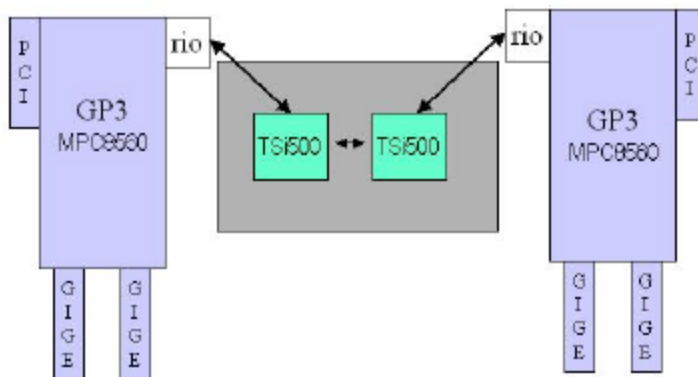
HIPPS is the first complete software development system for Rapid IO and includes the following components:

- 2 PowerQUICC 3 MPC8560 GP3 CPU cards
- 1 HIPPS Motherboard licensed from Tundra Semiconductor
 - Socket for up to 4 endpoints (2 per switch)
 - Tundra Semiconductor TSi500 switches
- ATX power supply and case
- Choice of OS/Software environment and tools license
 - Open source linux
 - QNX Momentics 6.3 Professional Edition
 - OSE Integrated Development Environment
- Rapid IO demo applications source code
- Technical support

HIPPS:

- Hardware
- Interoperability
- Platform
- Prototyping
- System

HIPPS Block Diagram

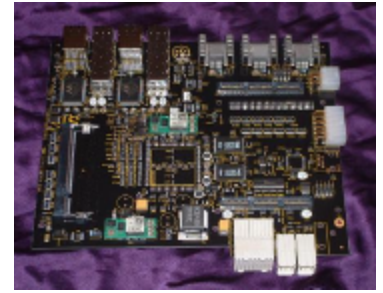


HIPPS SOFTWARE FEATURES:

- U-Boot with options to boot boards individually or have one running board boot the other.
- Memory mapped RIO configuration software for message passing functions between the two boards as well as providing RIO status/management
- The standard GCC development tools that will run on RedHat/x86 version 9.0, or PowerPC/Yellowdog version 3.1 for Linux, Windows XP for QNX and OSE
- Flash file systems that will boot up a demo mode of the HIPPS upon power up.
- A lightweight target file system that can be loaded on the Linux host and remotely mounted on the GP3 as a root file system for development.

Freescale PowerQUICC III CPU Chip Features

e500 Book E compatible core available from 800 MHz up to 1.5 GHz
32-bit, dual-issue, superscalar, seven-stage pipeline
32 KB L1 data and 32 KB L1 instruction cache (MMUs)
256 KB on-chip L2 cache with direct mapped capability
SIMD extension with single precision floating point
High-performance RISC CPM; 1 Gbps aggregate bandwidth
Two UTOPIA Level II master/slave ports with multi-PHY support
Eight TDM interfaces (T1/E1), two TDM ports that can be interfaced with T3/E3
ATM transmission convergence layer (8 channels) and inverse multiplexing (IMA)
MII, Serial, and various other CPM comm. ports offloaded from the e500 core
Integrated high-performance security processor function (optional by CPU)



Choice of software Tools and IDE

GPL Linux and Integration Services- www.embeddededge.com
QNX Neutrino- with true SMP in the Momentics IDE www.QNX.com
OSE- the ultra-reliable message passing solution www.OSE.com

Choice of Emulator Debugger Tool

Abatron BDI2000- on chip debugger- www.ultsol.com
i4000 OCD on chip debugger www.isystem.com

GP3 Physical Specification

Board Size

6.75x8.00 Inches
EBX compatible mechanical mounting holes

Operating Temperature

Standard version 0 to 40deg C
Extended temperature TBD

Weight

375g

Power Requirement

60Watt compact wall supply included



With every board you get:

wall power supply, cable kit, warranty card, PDF schematic, and software resource CD.
Accessories (extra DRAM, cables, optical transceivers) available on STx website

Contact Us!

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WWW.SiliconTKx.com/products.htm
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Shipping July 2004
Price USD \$25,000.00

Individual HIPPS boards only
also available for sale - call us!