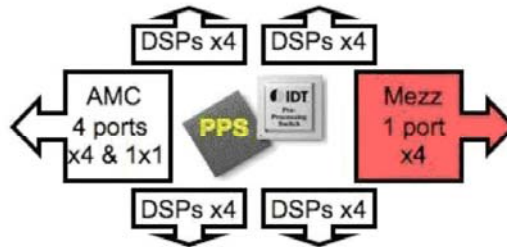


IDT 10gpps Serial Buffer "SERB Mezz"

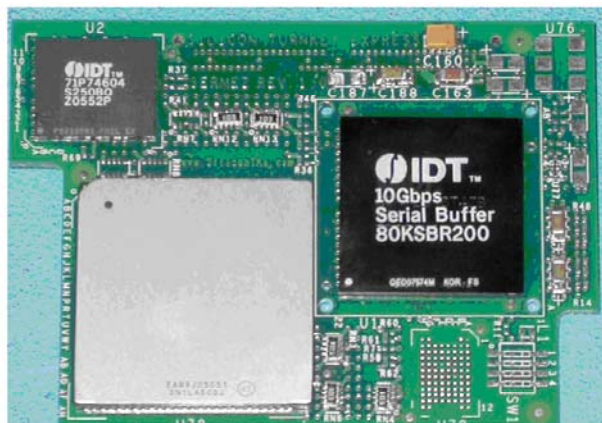
SERB
Mezz
Block
diagram



Adds
Enhanced
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Your IDT
AMC70K2000 w/
Quad TI DSP

The IDT RapidIO Serial Buffer is the first fabric IO memory device. Connected to an FPGA or ASIC, it is a powerful solution to manage and police a very high speed data stream without impeding traffic flow. Some applications include:

- Stateful Packet Management,
- Radio Base band processing functions
- Wireless infrastructure system throughput
- And many others



Red arrows show board to board connection (on bottom side of SERB MEZZ)



All this for just 3995\$ (price varies with FPGA selection)
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