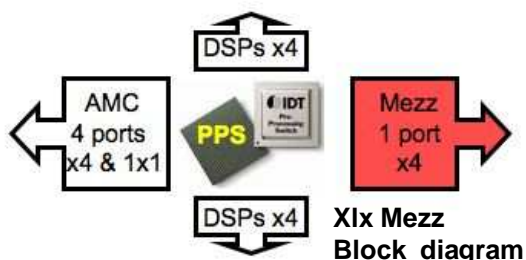


XLX Mezz

Xilinx Serial RapidIO Mezzanine Card

Customize your AMC70K2000 with a Xilinx FPGA daughter card using the V4FX60FF1152C.



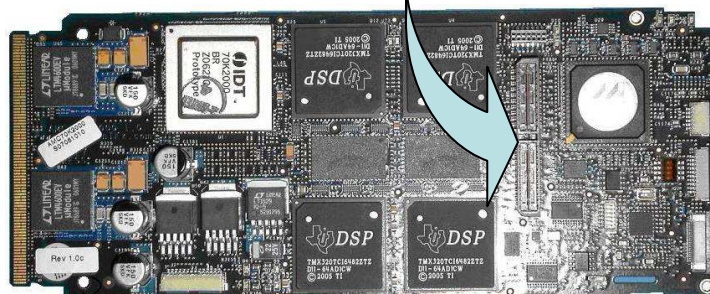
When the SRIO Core library* is uploaded, the XLX Mezz card becomes an SRIO endpoint capable of processing SRIO traffic data for the AMC70K2000 through the IDT PPS switch. The remaining FPGA gates can be coded to provide a wide variety of co-processor tasks including:

- Security algorithms,
- Base band processing functions
- Fast-path networking
- And many others

Xlx Mezz



**Note: The Xilinx's SRIO Core library in the V4FX 60 FF1152C has been tested for interoperability with the AMC70K2000.*



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NEW FROM STX

Xilinx FPGA LVDS Expander KIT

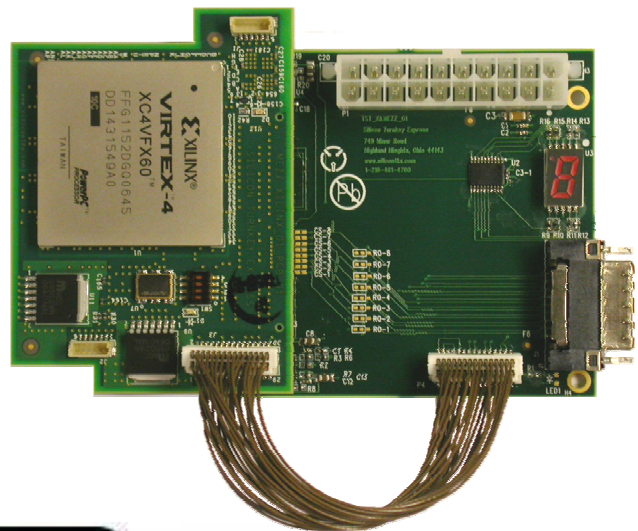
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- Expansion Adapter

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