



jPOD3000

jtag POver Debugger

Overview

The JTAG POver Debugger, jPOD, is an enhanced GNU debugger (GDB) for NXP (Freescale) 32 and 64 bit CPU based targets using JTAG, COP, OnCE, or BDM. It connects to the host computer by Ethernet. Debug cables provide a physical and electrical interface to the target processor. The first jPOD product being introduced is jPOD3000.

Features

- Auto-update system (to support new CPUs & CPU features)
- Source level debugging with GDB via BDM/JTAG interface
- Supports all CPU core speeds
- Allows debug code in cache, ROM, RAM, and FLASH memory
- Supports Linux kernel debugging
- Remote network debugging (GDB proxy)
- Built-in on-board programming of popular flash memories
- Additional debug and control commands
- Supports Linux hosts
- Supports 10/100 Ethernet network connection
- Supports USB 2.0 high-speed connection
- USB powered
- 3 years hardware warranty and 1 year software warranty support

Supported Target Families

ARM and other architecture are available, contact us.

MPC5xxx/8xx	MGT5100, MPC51xx, 52xx, 55xx (Nexus Class 1, no VLE support), 56xx, 57xx, 8xx
MPC74xx/86xx	MPC7400/10, 744x, 745x, 8640/D, 41/D, PPC6xx/7xx
MPC82xx	8245, 47, 48, 70, 71, 72, 75, 80
MPC83xx	8306s, 8, 11, 13, 14, 15, 21, 23, 43, 47, 49, 58, 60, 78, 79
MPC85xx	8533, 35, 40, 41, 43, 44, 45, 47, 55, 67
Qonverge	B4420, B4460, B4860, BSC9131, BSC9132, G1110, G4860
QorIQ_LS	LS102XA, 104XA, LS2040A, 2045A, 2080A, 2085A: (limited support)
QorIQ_Px	P1010 to 17, 20 to 25, 2010, 20, 40, 41, 3041, 4040, 80, 5010, 20, 21, 40
QorIQ_Tx	T1020, 22, 23, 24, 25, 40, 42, 2080, 81, 4160, 4240
Crypto, Starcore	C29x & MSC7116, 8101, 13, 22, 26, 44 (bare metal debug only)
ColdFire:	All (MCF5204, MCF5206(e) and MCF5272 are not supported)

JPOD does not support ddr/serdes validation, advanced trace, or packet protocol and network analysis tools



jPOD3000



GDB
The GNU Project
Debugger

Software Debug Capabilities

The jPOD3000 GDB functions also include:

- Setup and Configuration (local and remote)
- Initialization (Host and Target)
- FLASH
- Register
- GDB Interaction
- Telnet
- Installer/Updater

Specification

Power Supply:	5VDC @ 500mA
Probe power from target:	Less than 50mA
Target voltage levels supported:	1.2V to 3.3V; 5V tolerant
Operating temperature:	0 to 40 C
Storage temperature:	-40 to 70 C
Relative Humidity (non-condensing):	5% to 95%
Size:	8.5 x 4.75 x 2.33 in. (217 x 121 x 60 mm)
Certifications:	CE; RoHS

Ordering Information

JTAG Probe Open Debugger:	SKU #USI01-10201K
Cable Kit - USB, Ethernet & Serial:	SKU #USI06-60301K
Power Supply, International:	SKU #USI07-20301K

Target Connections (includes adapters and cables, emulates legacy debugger pinouts and connectors)

JTAG/COP (16 pin 0.1" Header)	SKU # USTAC-COP
OnCE (14 pin 0.1" Header)	SKU # USTAC-STC
ColdFire BDM (20 pin 0.1" Header)	SKU # USTAC-CTX

Availability in the Americas Q2 2016