

ADS512101 CPLD Revision History for PCB rev 4

01/09/09

REV	Released	Description
1	07/24/08	Based on CPLD rev 9 for PCB rev 3, Improved IRQs, Added RTC_INT#, added DDR/hibernate support
2	08/13/08	Changed NAND FLASH page size, Simplified HRST & SRST
3	08/22/08	Hibernate IRQ work around
4	08/27/08	Implemented hibernate function
5	09/10/08	Changed FLASH reset to assert during POR
6	12/12/08	Fix +3.3V immediate power up problem

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