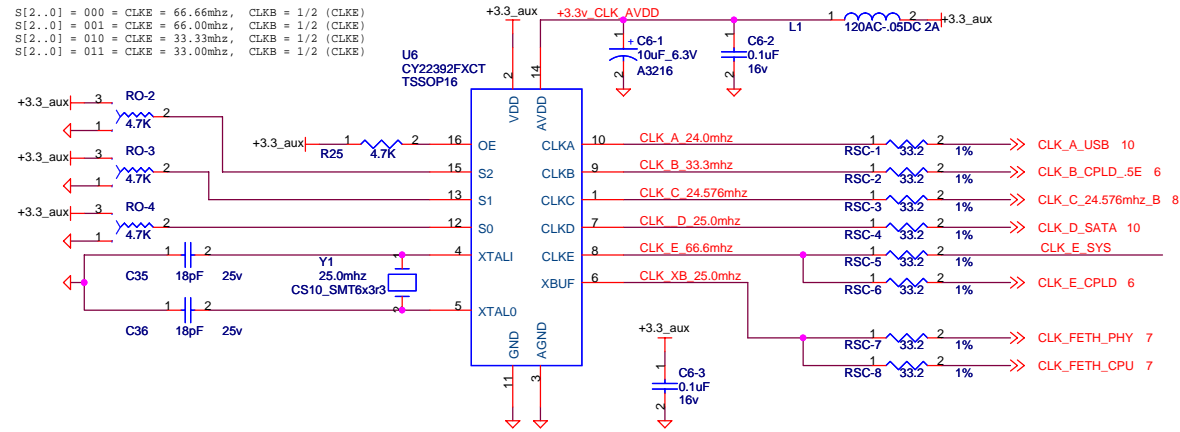
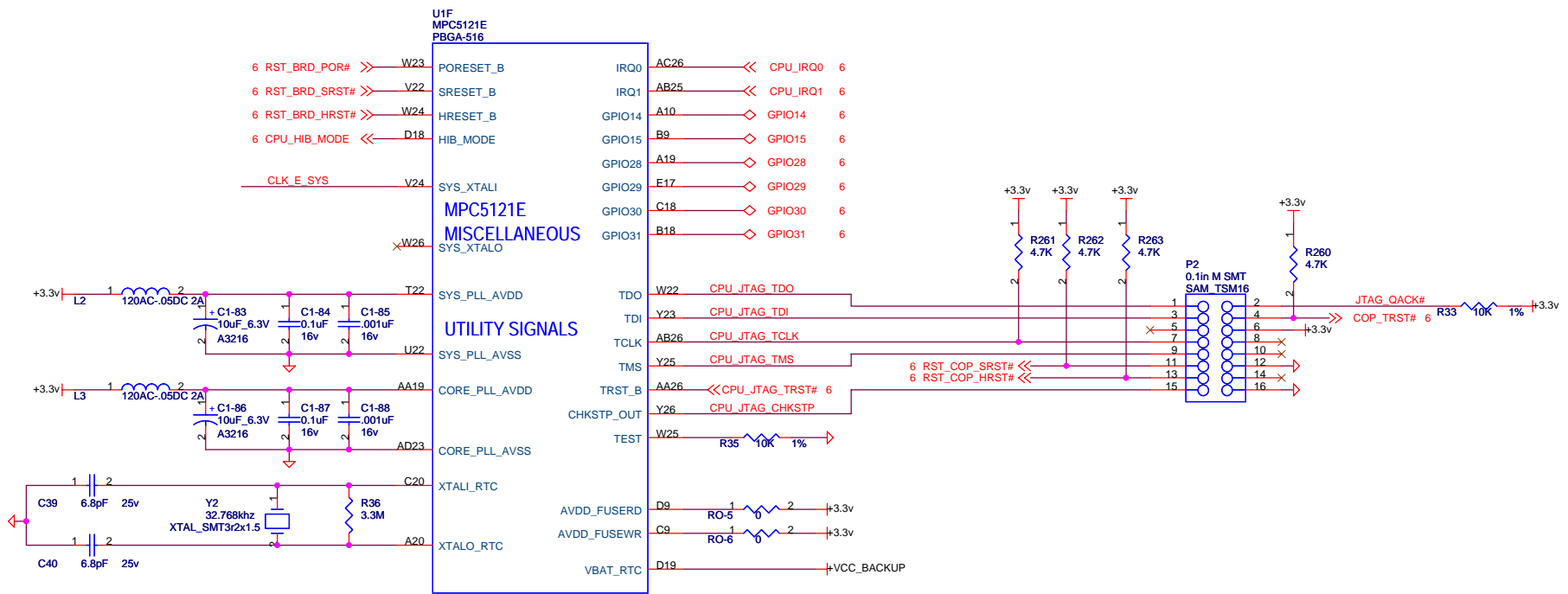
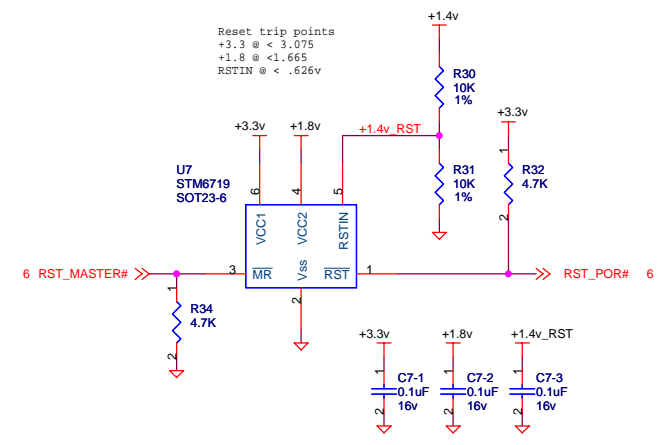
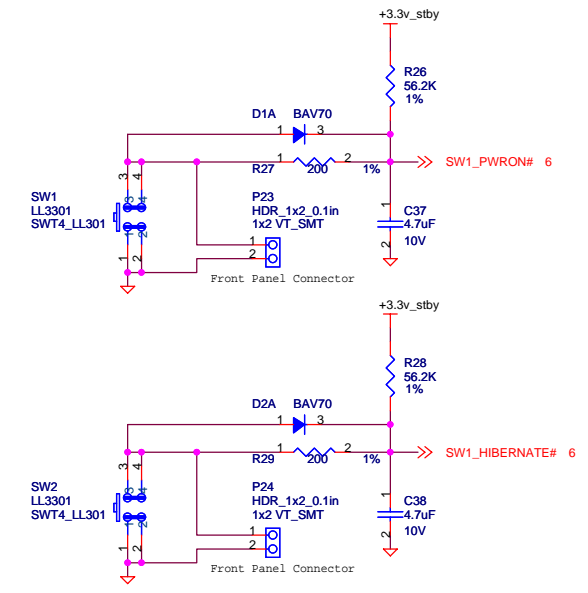
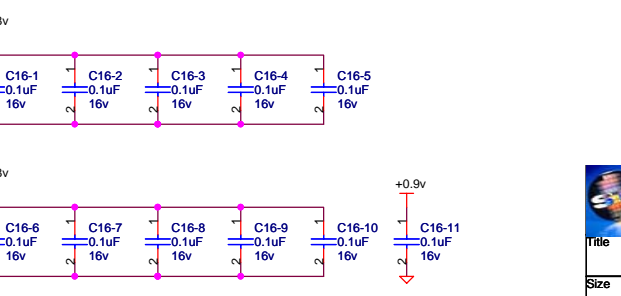
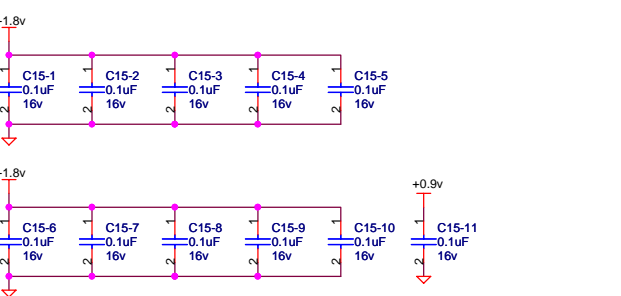
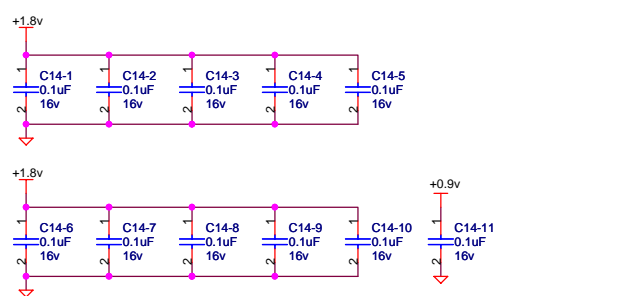
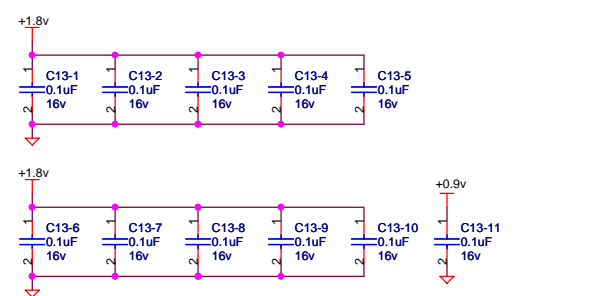
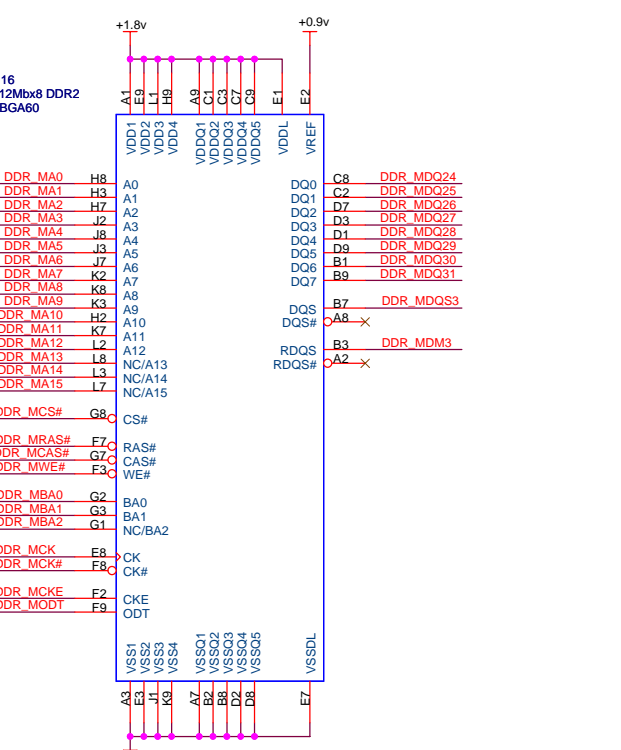
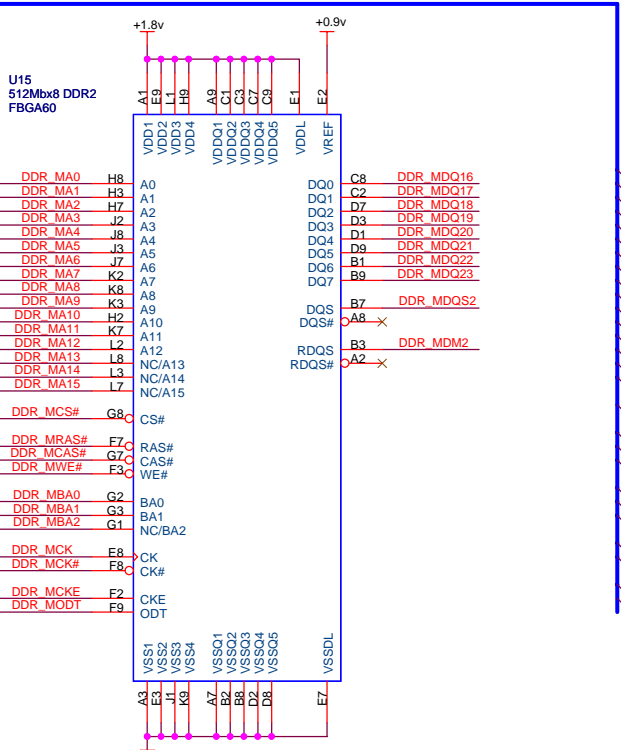
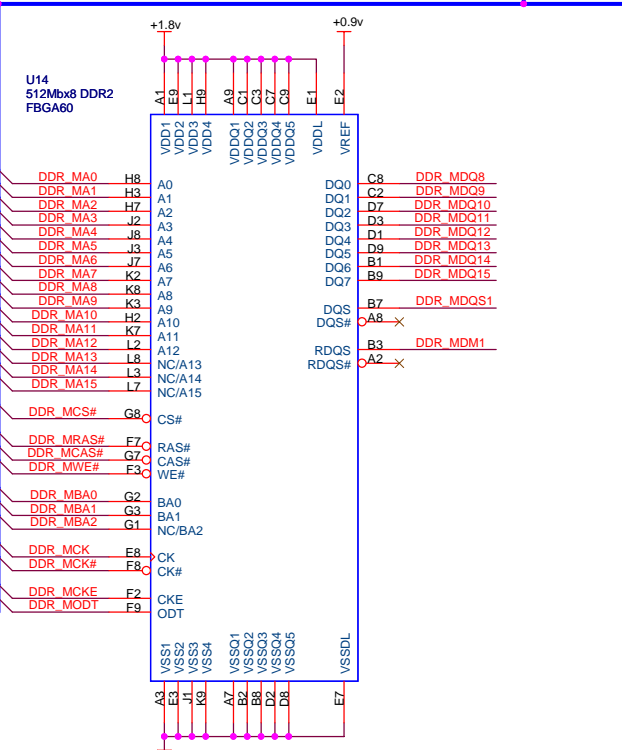
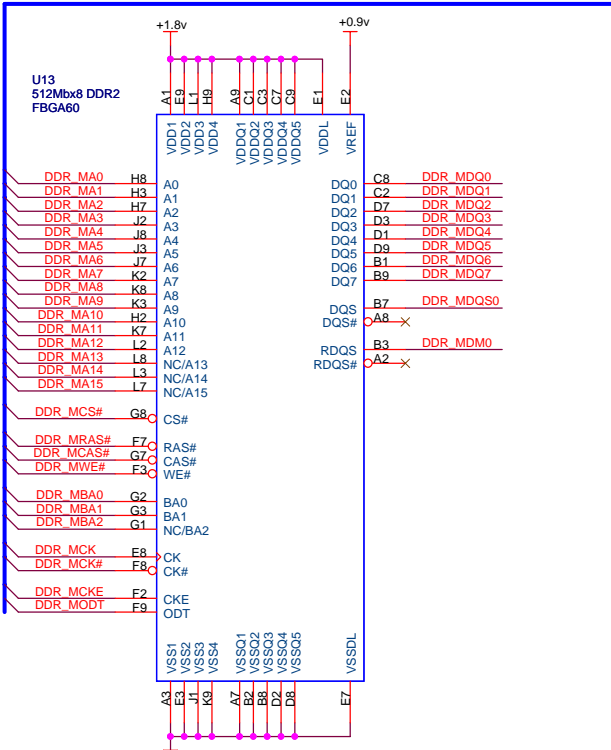
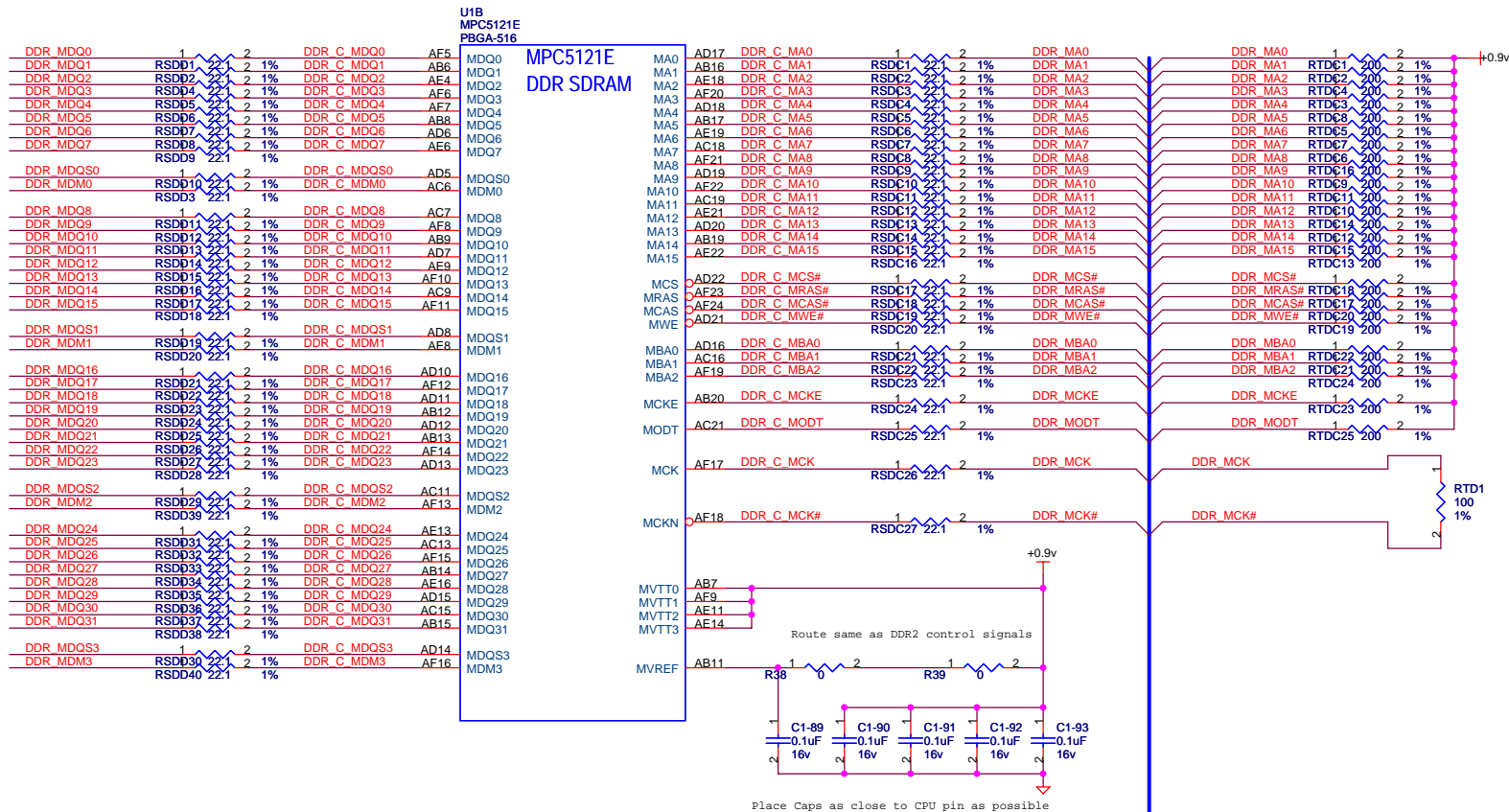


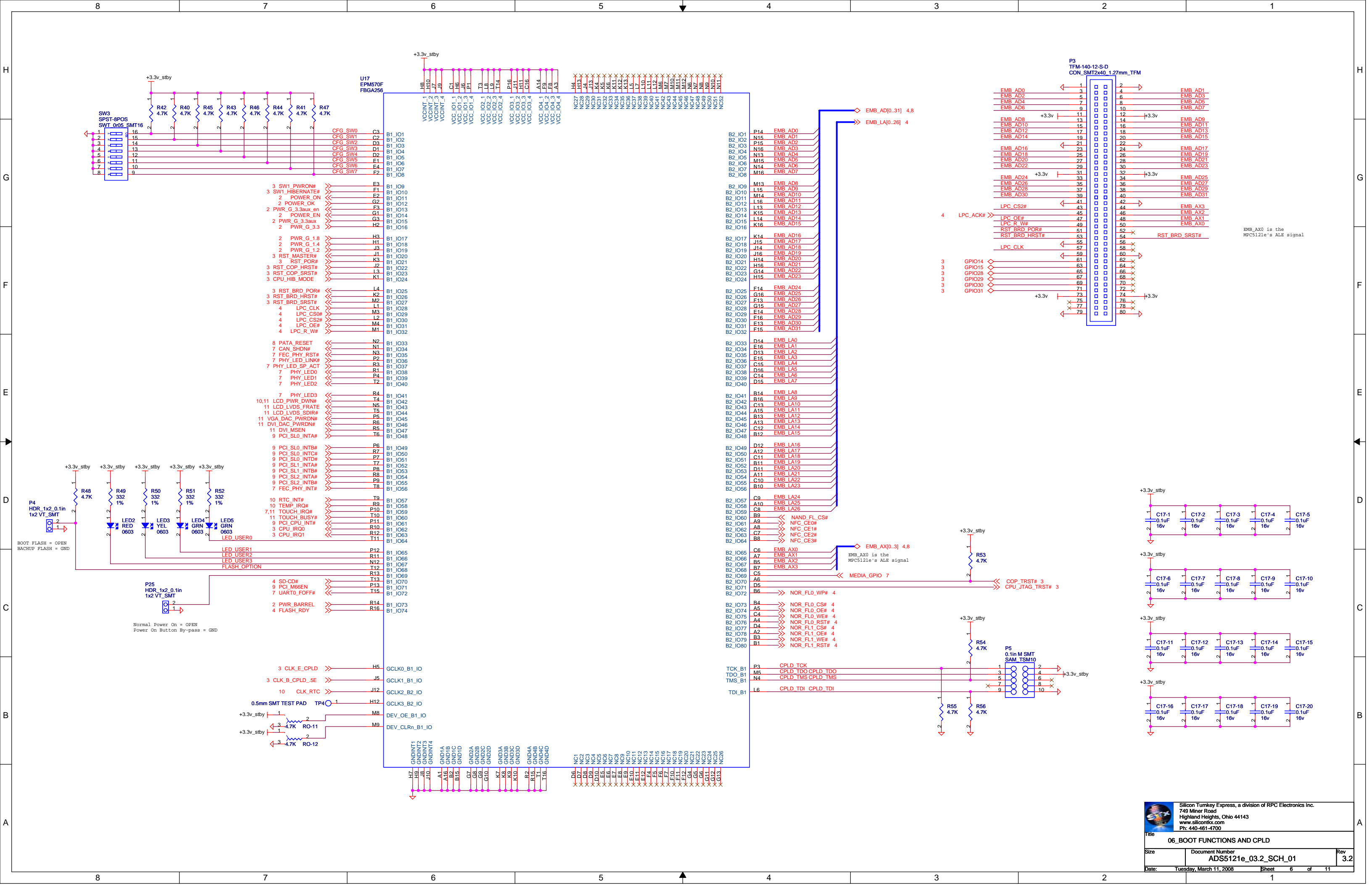
S[2..0] = 000 = CLKE = 66.66mhz, CLKB = 1/2 (CLKE)
 S[2..0] = 001 = CLKE = 66.00mhz, CLKB = 1/2 (CLKE)
 S[2..0] = 010 = CLKE = 33.33mhz, CLKB = 1/2 (CLKE)
 S[2..0] = 011 = CLKE = 33.00mhz, CLKB = 1/2 (CLKE)



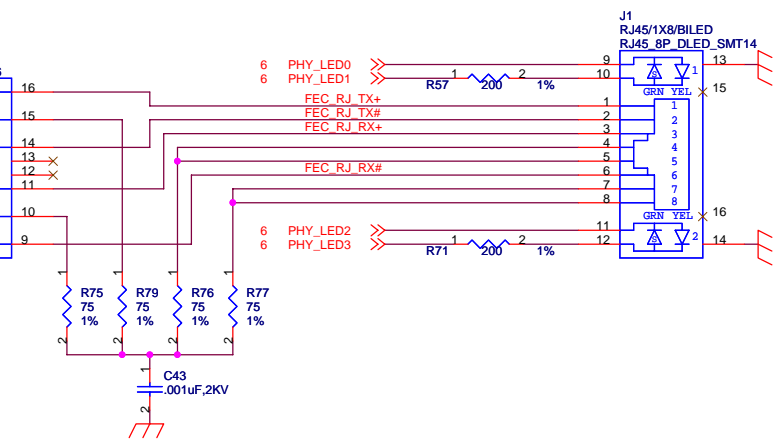
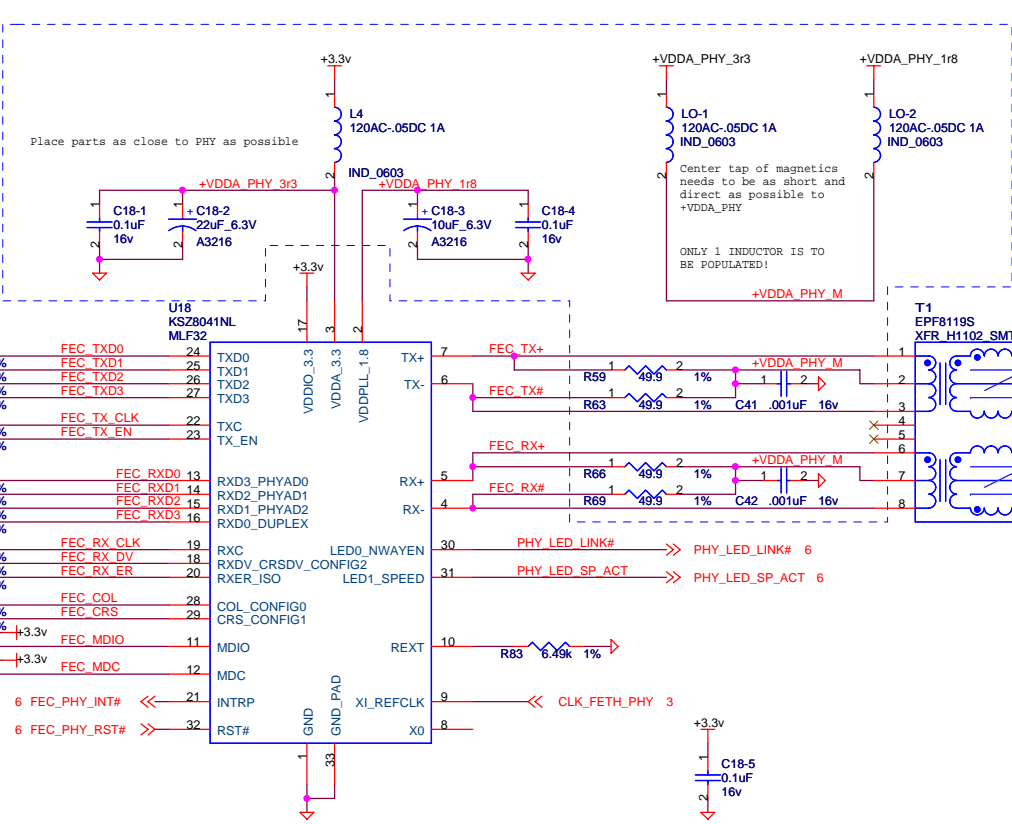
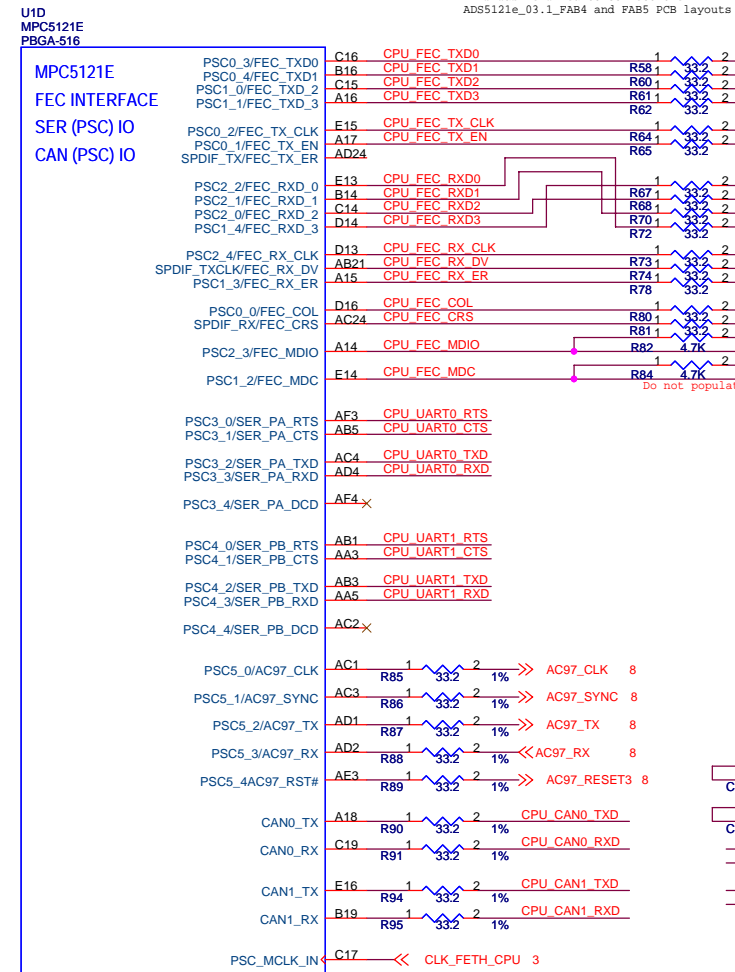
CLOCK RULES:
 All Clock traces are to be as short as possible
 CLK_E_SYS = CLK_E_CPLD
 CLK_FETH_PHY = CLK_FETH_CPU



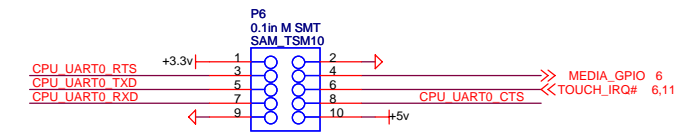




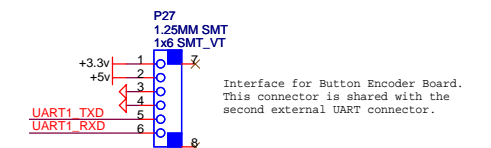
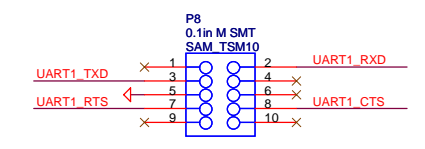
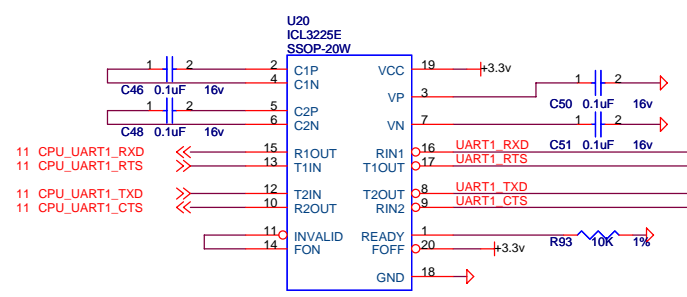
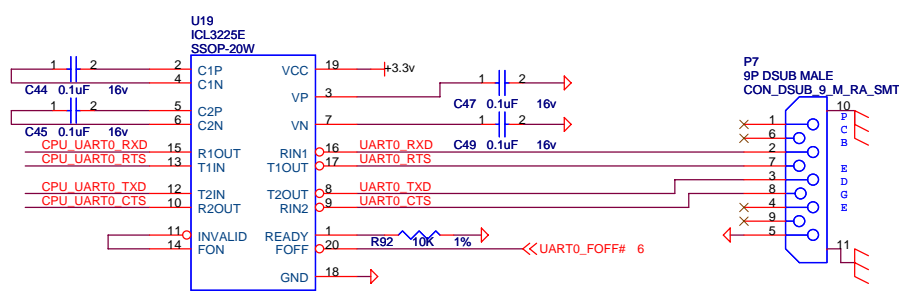
10/100 BASET ETHERNET PHY INTERFACE AND CONNECTOR



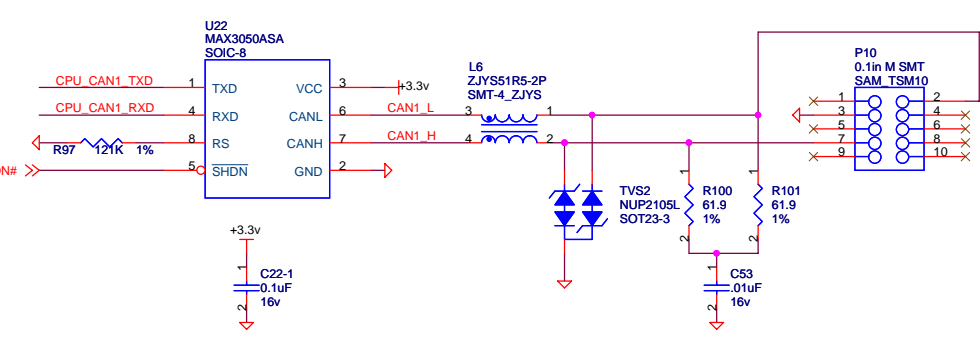
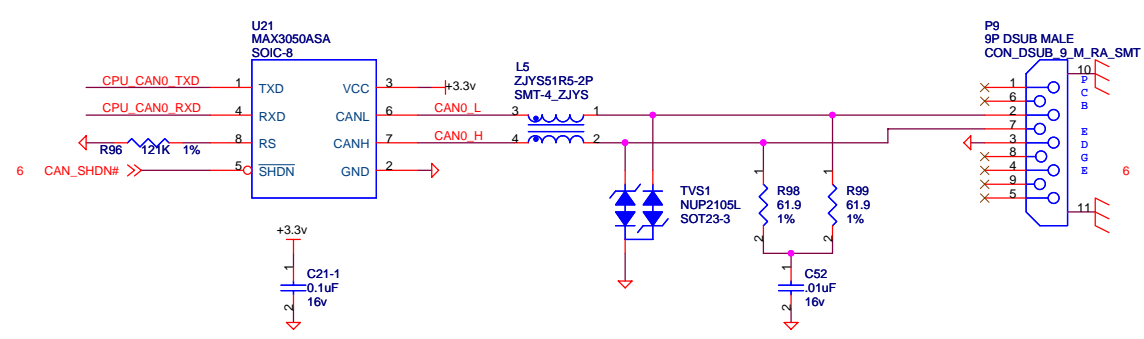
MEDIA_CONNECTOR



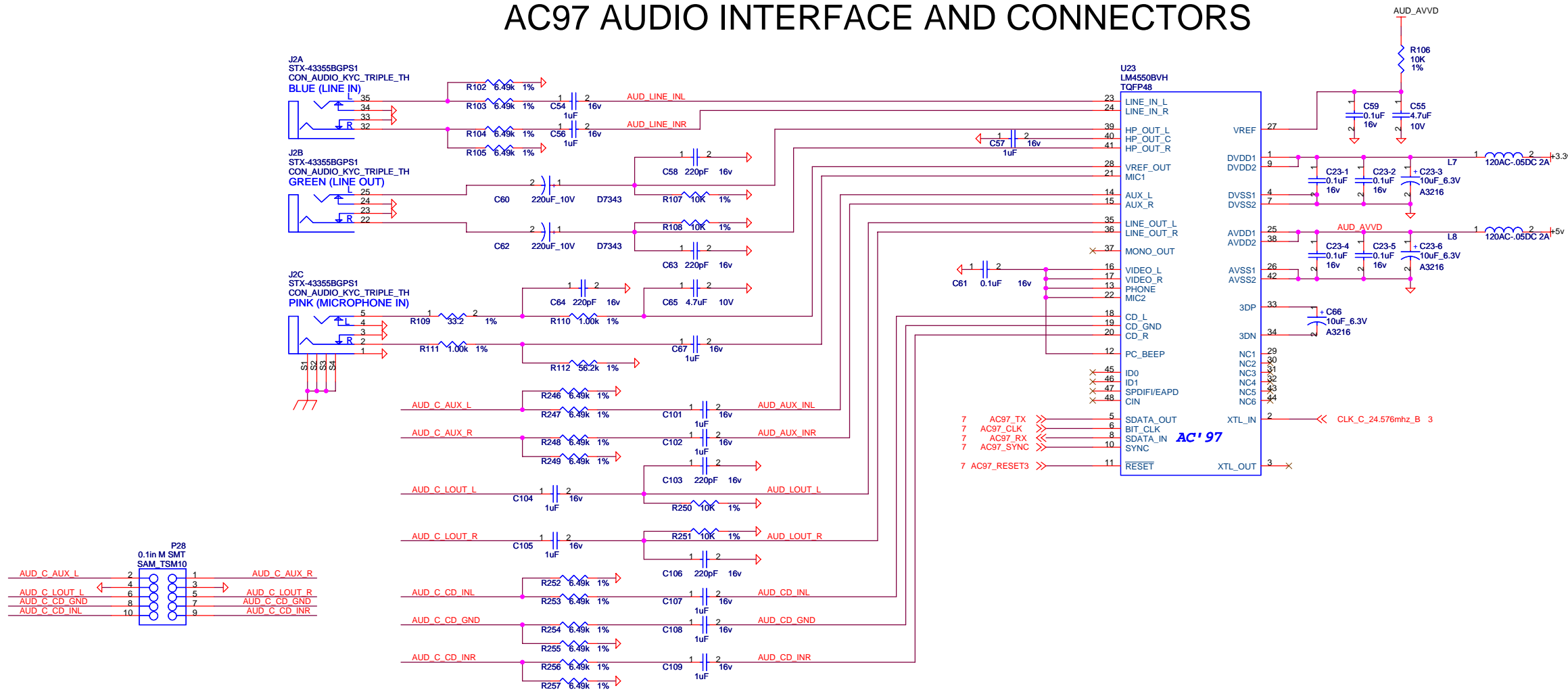
RS232 UARTS



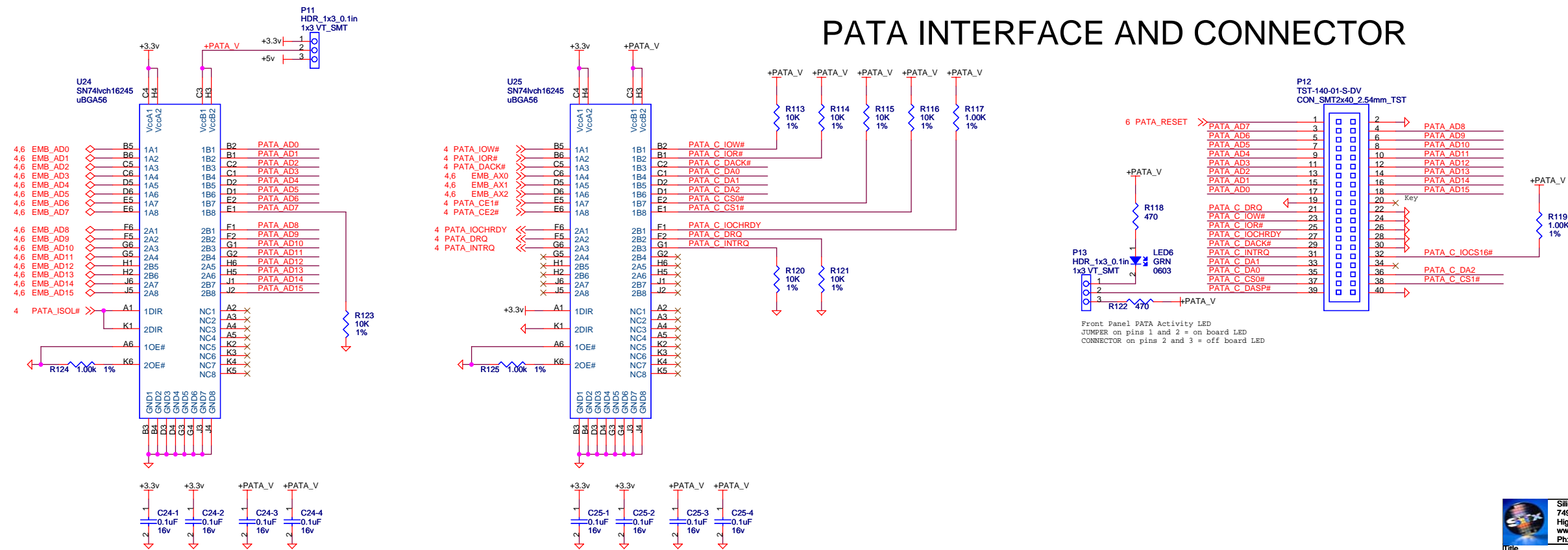
CAN BUS

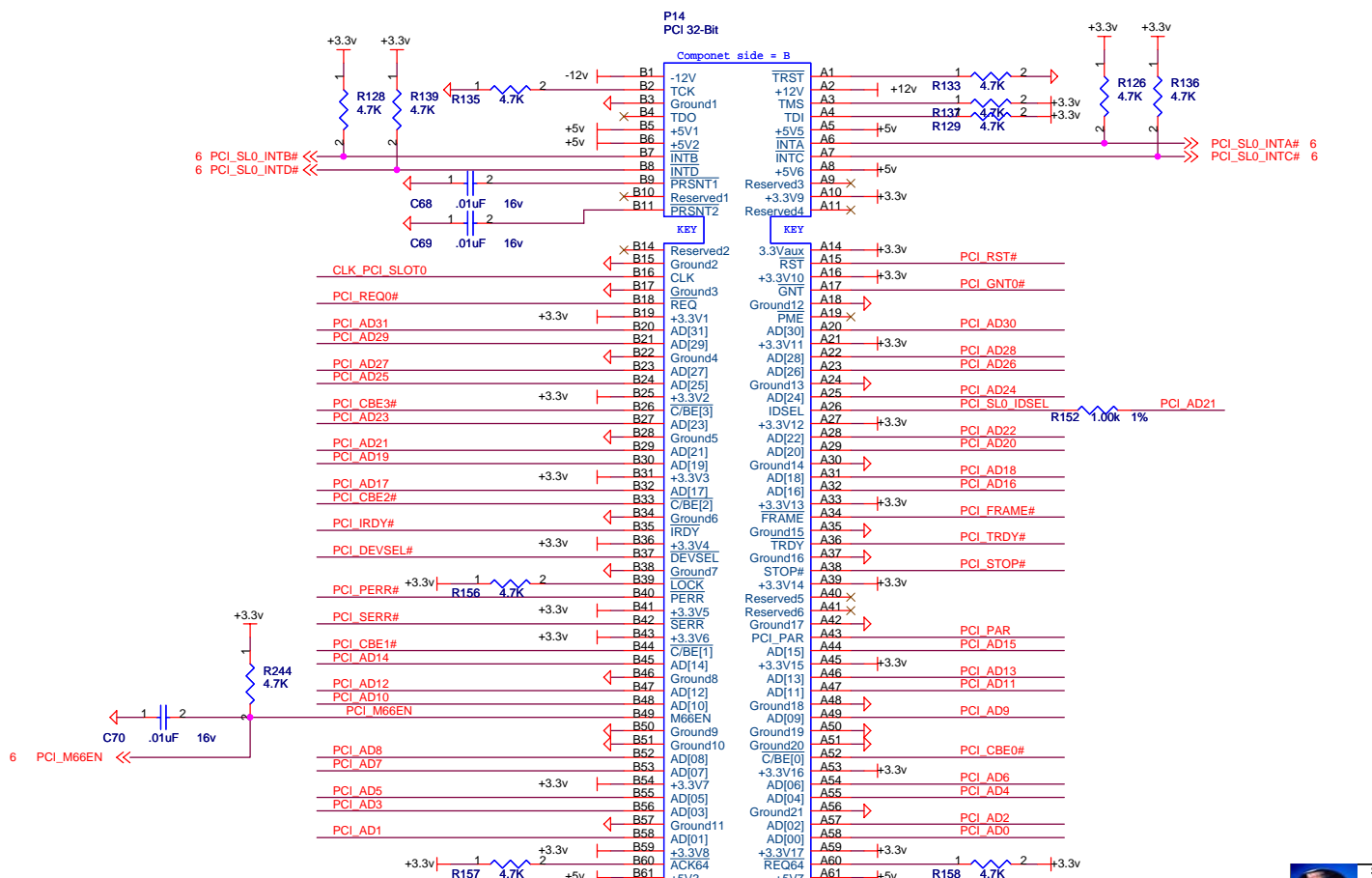
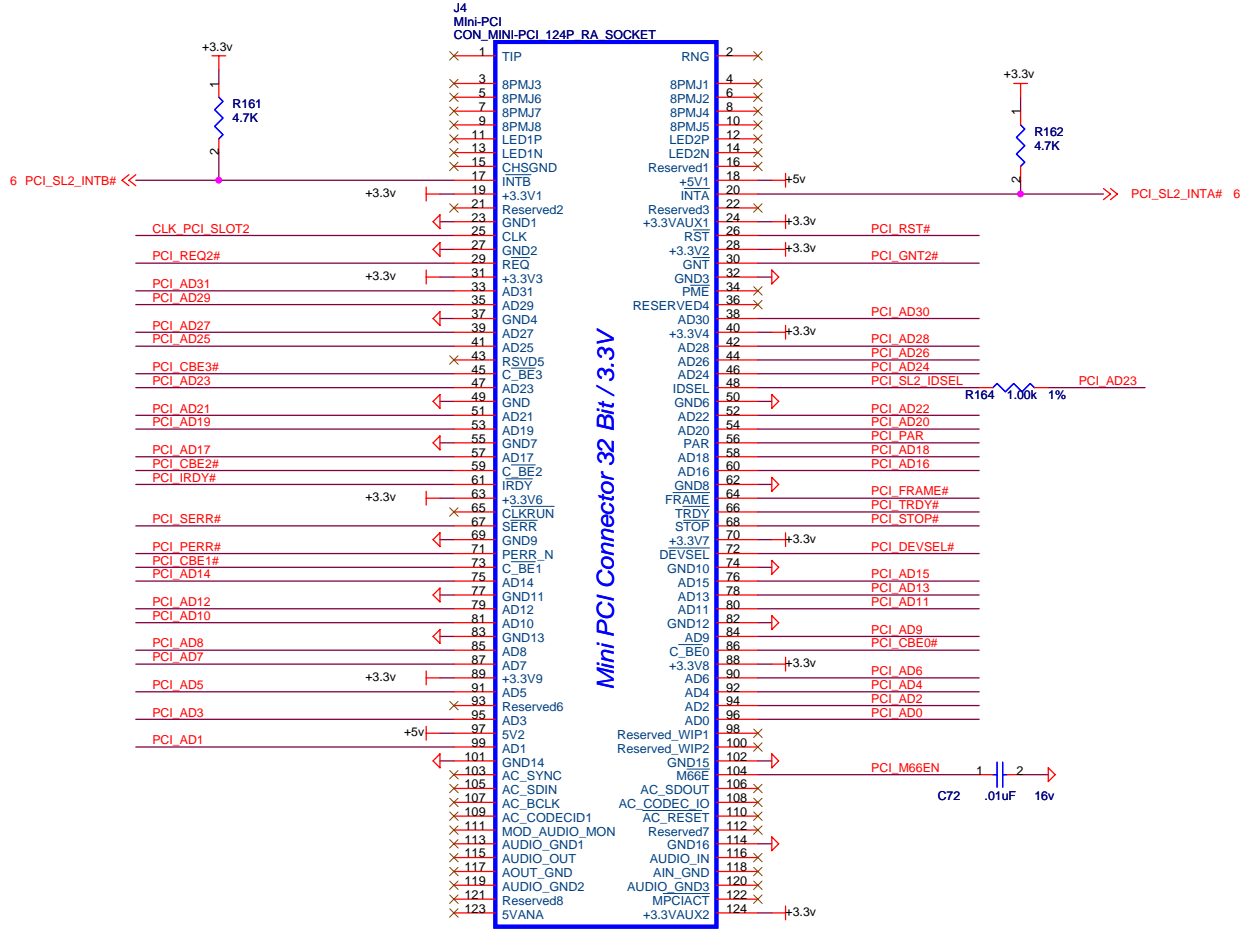
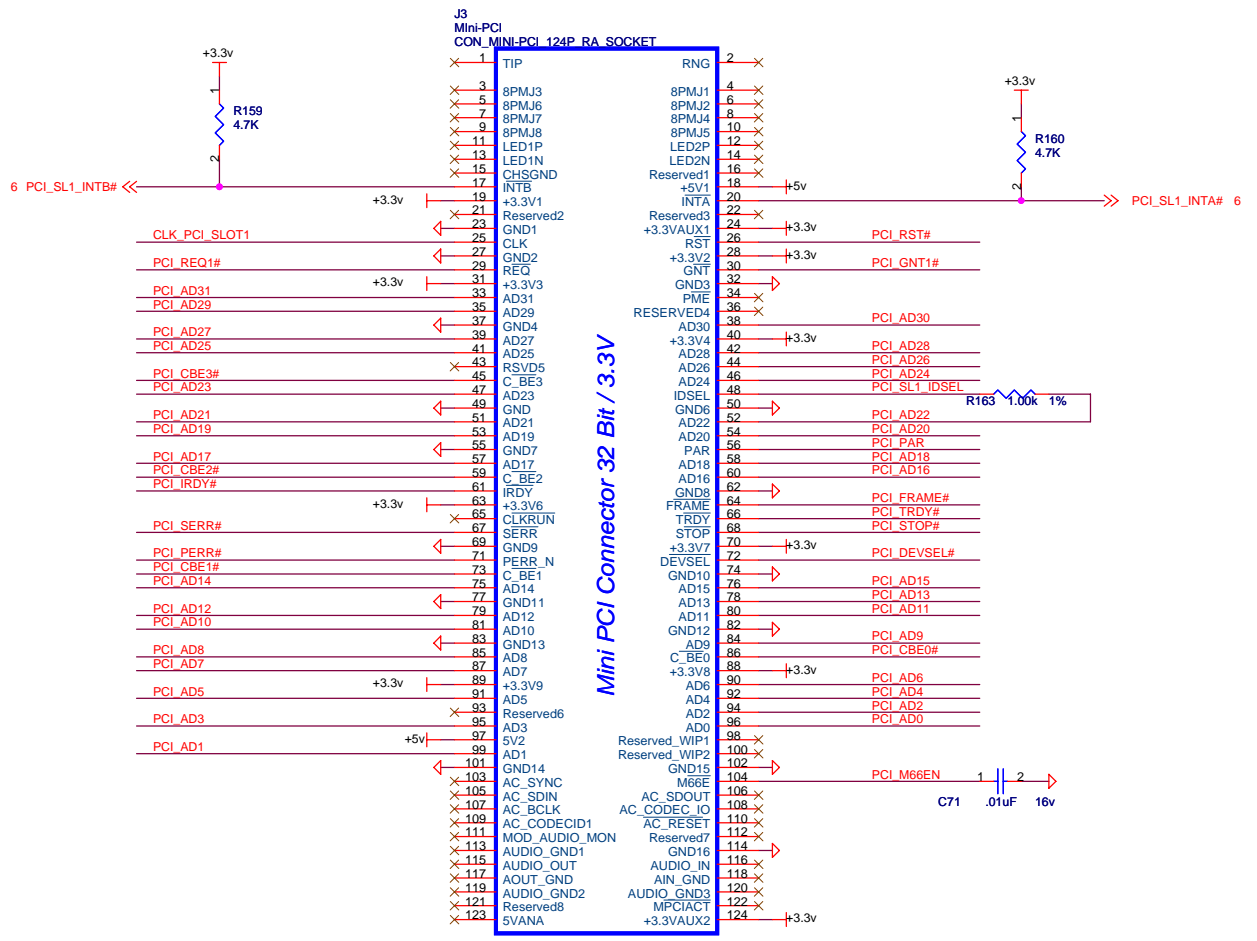
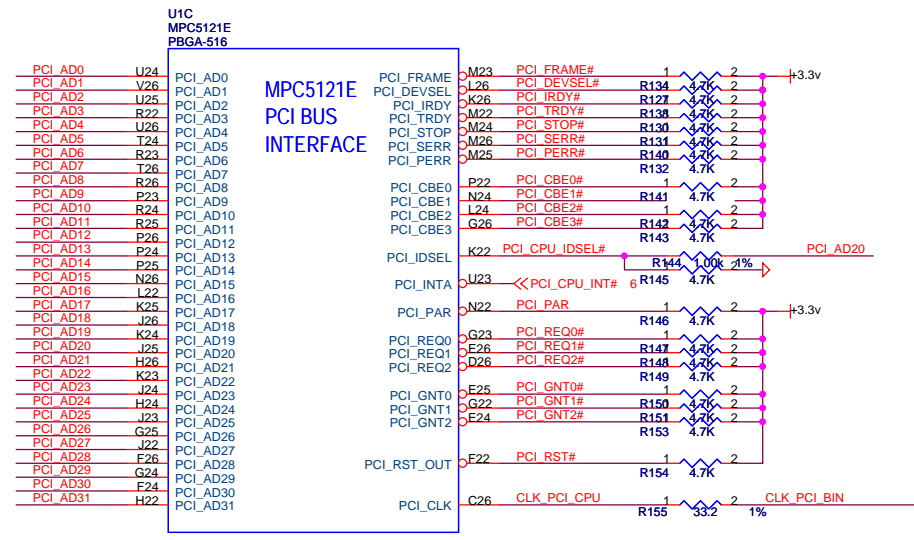


AC97 AUDIO INTERFACE AND CONNECTORS



PATA INTERFACE AND CONNECTOR





Silicon Turnkey Express, a division of RPC Electronics Inc.
749 Mier Road
Highland Heights, Ohio 44143
www.silicontx.com
Ph: 440-461-4700

SATA, USB AND I2C INTERFACES

